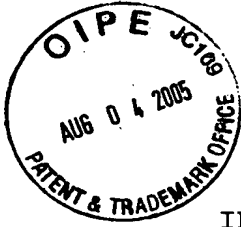


08-8-05

an B



PATENTS
MIC-40

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

PATENT APPLICATION

Applicant : Seong-Hoon Lee
Application No. : 10/722,959 Confirmation No.: 9002
Filed : November 26, 2003
For : DIGITAL DELAY-LOCKED LOOP CIRCUITS
WITH HIERARCHICAL DELAY ADJUSTMENT

Group Art Unit : 2816
Examiner : An T. Luu

New York, New York 10020
August 4, 2005

Mail Stop ISSUE FEE
Commissioner for Patents
P.O. Box 1450
Alexandria, Virginia 22313-1450

EXPRESS MAIL CERTIFICATION

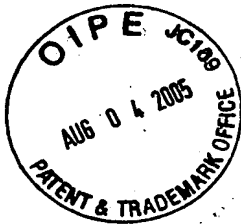
EXPRESS MAIL LABEL NO. EV619616949US

Date of Deposit: August 4, 2005.

I hereby certify that this paper/fee is being deposited with the United States Postal Service "EXPRESS MAIL POST OFFICE TO ADDRESSEE" service under 37 C.F.R. 1.10 on the date indicated above and is addressed to Mail Stop ISSUE FEE, Commissioner for Patents, P.O. Box 1450, Alexandria, Virginia 22313-1450.

Enclosures:

1. Transmittal Letter (3 pp. - in duplicate);
2. Amendment After Allowance Under 37 C.F.R. § 1.312 (15 pp.);
3. Comments On Statement Of Reasons For Allowance (4 pp.); and
4. Return Postcard.



Express Mail Label
No. EV619616949US

MIC-40

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Seong-Hoon Lee
Application No. : 10/722,959 Confirmation No.: 9002
Filed : November 26, 2003
For : DIGITAL DELAY-LOCKED LOOP CIRCUITS WITH
HIERARCHICAL DELAY ADJUSTMENT
Group Art Unit : 2816
Examiner : An T. Luu

New York, New York 10020
August 4, 2005

Mail Stop ISSUE FEE
Commissioner for Patents
P.O. Box 1450
Alexandria, Virginia 22313-1450

TRANSMITTAL LETTER

Sir:

Transmitted herewith: ☐ a Preliminary Amendment;
☐ Letter to Official Draftsperson; ☒ Amendment After
Allowance Under 37 C.F.R. § 1.312; ☒ Comments On Statement
Of Reasons For Allowance; ☐ Petition For Extension Of Time
Under 37 C.F.R. § 1.136(a); ☐ a Declaration; ☐ a Power of
Attorney; ☐ a Submission of Formal Drawings to be filed in
the above identified patent application.

FEE FOR ADDITIONAL CLAIMS

☒ A fee for additional claims is not required.

☐ A fee for additional claims is required.

The additional fee has been calculated as shown below:

	CLAIMS REMAINING AFTER AMENDMENT		HIGHEST NUMBER PREVIOUSLY PAID FOR		PRESENT EXTRA		RATE		ADDITIONAL FEES
TOTAL CLAIMS	37	-	37	* =	0	X	\$ 50	=	\$ 0.00
INDEPENDENT CLAIMS	10	-	10	** =	0	X	\$ 200	=	\$ 0.00
FIRST PRESENTATION OF A MULTIPLE DEPENDENT CLAIM							+ \$ 360	=	\$ 0.00

* If less than 20, insert 20. TOTAL \$ 0.00
 ** If less than 3, insert 3.

[] A check in the amount of \$ _____ in payment of the additional claims is transmitted herewith.

[] Please charge \$ _____ to Deposit Account No. _____ in payment of the filing fee.

[X] The Director is hereby authorized to charge payment of any additional filing fees required under 37 C.F.R. § 1.16, in connection with the paper(s) transmitted herewith, or credit any overpayment of same, to Deposit Account No. 06-1075, Order No. 002322.0040. A duplicate copy of this transmittal letter is transmitted herewith.

EXTENSION FEE

[] The following extension is applicable to the Response filed herewith; [] \$120.00 extension fee for response within first month pursuant to 37 C.F.R. § 1.136(a); [] \$450.00 extension fee for response within second month pursuant to 37 C.F.R. § 1.136(a); [] \$1020.00 extension fee for response within third month pursuant to 37 C.F.R. § 1.136(a); [] \$1590.00 extension fee for response within fourth month pursuant to 37 C.F.R. § 1.136(a); [] \$2160.00 extension fee for response within fifth month pursuant to 37 C.F.R. 1.136(a).

☐ A check in the amount of ☐ \$120.00; ☐ \$450.00;
☐ \$1020.00; ☐ \$1590.00; ☐ \$2160.00; in payment
of the extension fee is transmitted herewith.

☐ Please charge the ☐ \$120.00; ☐ \$450.00;
☐ \$1020.00; ☐ \$1590.00; ☐ \$2160.00; extension
fee to Deposit Account No. _____. A duplicate
copy of this transmittal letter is transmitted
herewith.

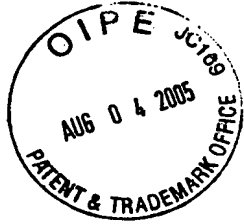
☒ The Director is hereby authorized to charge payment of
any additional fees required under 37 C.F.R. § 1.17 in
connection with the paper(s) transmitted herewith, or to
credit any overpayment of same, to Deposit Account No.
06-1075, Order No. 002322.0040. A duplicate copy of this
transmittal letter is transmitted herewith.

Respectfully submitted,

Evelyn C. Mak

Evelyn C. Mak
Registration No. 50,492
Attorney for Applicant

FISH & NEAVE IP GROUP
ROPES & GRAY LLP
Customer No. 1473
1251 Avenue of the Americas
New York, New York 10020-1105
(212) 596-9000



Express Mail Label
No. EV619616949US

MIC-40

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

PATENT APPLICATION

Applicant : Seong-Hoon Lee
Application No. : 10/722,959 Confirmation No.: 9002
Filed : November 26, 2003
For : DIGITAL DELAY-LOCKED LOOP CIRCUITS WITH
HIERARCHICAL DELAY ADJUSTMENT
Group Art Unit : 2816
Examiner : An T. Luu

New York, New York 10020
August 4, 2005

Mail Stop ISSUE FEE
Commissioner for Patents
P.O. Box 1450
Alexandria, Virginia 22313-1450

COMMENTS ON STATEMENT OF REASONS FOR ALLOWANCE

Sir:

The above-identified patent application was allowed on July 21, 2005. The Notice of Allowability included a Statement of Reasons for Allowance.

As part of the Statement, the Examiner stated (on page 2 of the Notice of Allowability) that "none of the prior art teaches or fairly suggests" and then went on to set forth features from applicant's claims. However, the language set forth by the Examiner does not accurately reflect what is recited in independent

claims 1, 8, 16, and 35-37.¹ To the extent that the language set forth by the Examiner differs from the actual claim features, applicant respectfully submits that his invention should be limited only by the claim language itself and not by the language that appears in the Statement of Reasons for Allowance.

To assist the Examiner, applicant sets forth in the tables below his claim features as compared to the language set forth in the Statement of Reasons for Allowance:

Claims 1, 8, and 35	Statement of Reasons for Allowance
... generating a third signal having a fifth phase between said third phase and said fourth phase, said fifth phase determined by said third control signal.	... generating a third signal having a fifth phase between said third phase and said fourth phase, said fifth phase determined by said fourth control signal ...

Claims 16 and 37	Statement of Reasons for Allowance
a) [means for] generating a first output signal having a first phase between the phases of first and second input signals;	generating a first signal having a third phase between said first phase and said second phase, said third phase determined by said first control signal;
b) [means for] generating a second output signal having a second phase not equal to said first phase between the phases of said first and second input	generating a second signal having a fourth phase between said first phase and said second phase, said fourth phase determined by

¹ Note that the language of independent claims 1 and 35 is based on the claim amendments made in the Amendment After Allowance Under 37 C.F.R. § 1.312 being filed concurrently herewith.

<p>signals;</p> <p>c) [means for] repeating a) and b) a predetermined number of times greater than one, wherein ...; and</p> <p>d) [means for] generating a third output signal having a third phase between said first phase of said first output signal and said second phase of said second output signal generated from step c) after said predetermined number of times.</p>	<p>said second control signal; and</p> <p>generating a third signal having a fifth phase between said third phase and said fourth phase, said fifth phase determined by said fourth control signal ...</p>
---	--

Claim 36	Statement of Reasons for Allowance
<p>means for generating a third signal having a third phase between said first phase and said second phase;</p> <p>means for generating a fourth signal having a fourth phase between said first phase and said second phase, said fourth phase not equal to said third phase;</p> <p>means for generating a fifth signal having a fifth phase between said third phase and said fourth phase.</p>	<p>generating a first signal having a third phase between said first phase and said second phase, said third phase determined by said first control signal;</p> <p>generating a second signal having a fourth phase between said first phase and said second phase, said fourth phase determined by said second control signal; and</p> <p>generating a third signal having a fifth phase between said third phase and said fourth phase, said fifth phase determined by said fourth control signal ...</p>

Applicant respectfully invites the Examiner to issue a revised Statement of Reasons for Allowance using the exact language from the claims.

Respectfully Submitted,

Evelyn C. Mak

Evelyn C. Mak
Registration No. 50,492
Attorney for Applicant

FISH & NEAVE IP GROUP
ROPES & GRAY LLP
Customer No. 1473
1251 Avenue of the Americas
New York, New York 10020-1105
(212) 596-9000

Express Mail Label
No. EV619616949US

PATENTS
MIC-40



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE
PATENT APPLICATION

Applicant : Seong-Hoon Lee
Application No. : 10/722,959
Confirmation No. : 9002
Filed : November 26, 2003
For : DIGITAL DELAY-LOCKED LOOP CIRCUITS
WITH HIERARCHICAL DELAY ADJUSTMENT
Group Art Unit : 2816
Examiner : An T. Luu

New York, New York 10020
August 4, 2005

Mail Stop ISSUE FEE
Commissioner for Patents
P.O. Box 1450
Alexandria, Virginia 22313-1450

AMENDMENT AFTER ALLOWANCE UNDER 37 C.F.R. § 1.312

Sir:

Pursuant to 37 C.F.R. § 1.312, applicant respectfully requests entry of the following amendments of the above-identified patent application.

AMENDMENTS TO THE CLAIMS are reflected in the listing of claims which begins on page 2.

REMARKS begin on page 15.